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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Jean-Michel Daga et al.      PATENT APPLICATION  
Serial No.: 10/810,033      Group Art Unit: 2816  
Filed: March 26, 2004  
For: HIGH EFFICIENCY, LOW COST, CHARGE PUMP CIRCUIT

Supplemental Information Disclosure Statement  
with Certification under 37 CFR § 1.97(e)(1)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure under 37 CFR § 1.97(e). A copy of each of the cited reference is enclosed.

OTHER REFERENCES

*CMOS Charge Pumps Using Cross-Coupled Charge Transfer Switches with Improved Voltage Pumping Gain and Low Gate-Oxide Stress for Low-Voltage Memory Circuits* by K-S Min and Jin-Hong Ahn, IEICE Transactions on Electronics, Institute of Electronics Information and Comm. Eng. Tokyo, Japan, Vol. E85-C, No. 1, Janvier 2002, pages 225-229; XP001116786; ISSN 0916-8524.

*A Low-Ripple Switched-Capacitor DC-DC Up Converter for Low-Voltage Applications*, by Seung-Chul Lee and Seung-Hoon Lee, IEICE Transactions on Electronics, Institute of Electronics Information and Comm. Eng., Tokyo, Japan, Vol. E84-C, No. 8, August 2001, pages 1100-1103, XP001107649, ISSN: 0916-8524.

The undersigned hereby certifies that the items of information contained in this Supplemental Information Disclosure Statement were cited in a communication received from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

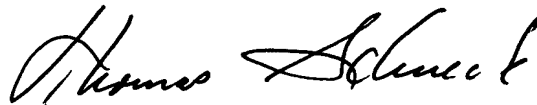
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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signed: Brenda Elmidolan  
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Date: August 25, 2004

Respectfully submitted,



Thomas Schneck

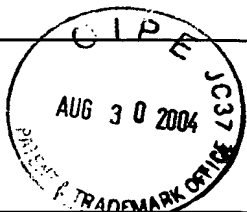
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FORM PTO-1449	Atty. Docket No. ATM-276	Serial No. 10/810,033
LIST OF PRIOR ART CITED BY APPLICANT	Applicants: Jean-Michel Daga et al.	
	Filing Date: March 26, 2004	Group: 2816



## U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Publ. Date	Name	Class	Sub Class	Filing Date
AA						
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## FOREIGN PATENT DOCUMENTS

Examiner Initial*	Document Number	Publ. Date	Country	Class	Sub Class	Translation Yes No
AK						
AL						
AM						
AN						

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AO	S-C Lee et al., A Low-Ripple Switched-Capacitor DC-DC up Converter for Low-Voltage Applications, IEICE Transactions on Elect., Instit. Electronics Information and Comm. Eng., August 2001, Vol. E84-C, No. 8, pages 1100-1103, XP001107649, ISSN 0916-8524.
AP	K-S Min et al., CMOS Charge Pumps Using Cross-Coupled charge Transfer Switches with Improved Voltage Pumping Gain and Low Gate-Oxide Stress for Low-Voltage Memory Circuits, IEICE Transactions on Electron., Instit. Electronics Information and Comm. Eng., January 2002, Vol. E85-C, No. 1, January 2002, XP001116786, ISSN 0916-8524.

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DATE CONSIDERED:

\*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.